ISTITUTO NAZIONALE DI RICERCA METROLOGICA
Repository Istituzionale

Project of an automated Multiple Decade Ratios Precision Divider for Generation of low DC Voltages

(Article begins on next page)

16 May 2024

Project of an Automated Multiple Decade Ratios Precision Divider for Generation of Low DC Voltages

Flavio Galliana[1], Roberto Cerri, Davide Corona
National Institute of Metrological Research, Turin, Italy

At the National Institute of Metrological Research (INRIM) an automatable precision DC voltage fixed-ratios divider allows the division ratios of DC Voltages from 10:1 to 10^7:1. It can be quickly calibrated when necessary and involved in traceability transfer. Its resistors are selected bulk metal foil ones connected in series in four terminal configuration whose values are 90 kΩ, 9 kΩ, 900 Ω, 90 Ω, 9 Ω, 0.9 Ω, 90 mΩ and 10 mΩ. The main advantage of this divider is that it can be automatically calibrated with a calibrator and a digital multimeter. Its calibration starts from a 10 V value. It takes advantage of the DMM linearity, in particular in the 10 V range that allows improving its calibration uncertainty. After calibration, it can be used to divide DC Voltages lower than 10 V. Preliminary evaluation of its calibration uncertainties and mid-term stabilities (a week) span respectively from 1.4×10^{-6} to 6.0×10^{-4} and from 2.4×10^{-7} to 4.5×10^{-4} for ratios from 10:1 to 10^7:1. This divider could be involved in the calibration of nanovoltmeters in a typical range from 10 V down to 100 nV.

1. Introduction

Today, the traceability from the DC Voltage standard to low and ultra-low values is still a challenge due to the needs in research, nanotechnology and medical frameworks. National and high level secondary laboratories have used for calibration of digital multi-meters (DMMs) and multifunction calibrators (MFCs) commercial high precision manually operating DC voltage dividers [1, 2] or a recently developed automated fixed ratios divider [3]. In addition, research on DC Voltage divider has been wide. Effective guarded dividers were developed for high accuracy DC voltage applications [4, 5] also for high voltages [6–10]. Problems arise for lower voltages. The widespread instrument to measure low DC Voltages is the nanovoltmeter. Modern nanovoltmeters are highly sophisticated and accurate instruments often involved in precise and highly advanced measurement applications. Unfortunately, many of them are not calibrated and used trusting in their manufacturer specifications. At high level, National Measurement Institutes (NMIs) nanovoltmeters are calibrated vs. the Josephson Voltage Standards (JVS) changing the microwave frequency [11, 12]. Nevertheless, this choice is expensive and time consuming. NMIs and Laboratories without the JVS need alternative solutions. Interesting and valid solutions with easily available instruments are proposed in [13, 14]. At National Institute of Metrological Research (INRIM) an automatable multiple decade ratios precision divider has been built which is currently being automated.

1   Corresponding author: f.galliana@inrim.it.

2. The INRIM DC Voltage Divider

This divider is made with 90 kΩ, 9 kΩ, 900 Ω, 90 Ω, 9 Ω, 0.9 Ω, 90 mΩ and 10 mΩ resistors connected in four terminal configuration. The advantage of this divider lies in its easy and quick calibration when necessary with a calibrated DC Voltage calibrator and a particular DMM, widespread in National Measurement Institutes (NMIs) and in electrical calibration laboratories as [15]. Once the divider is calibrated it can be involved in a measurement setup, with a DC Voltage reference standard or a calibrated MFC, for the generation of low DC Voltages and calibration of nanovoltmeters mainly in the range from 100 nV to 10 V. Its scheme is shown in Figure 1.

The divider has been made with high nominal power resistors with respect to the applied power during the divider calibration of ratios calibration. The ratio between these two powers has been kept as high as possible to neglect the effects of the power coefficients. It has been made with selected Vishay low temperature coefficient (TCR) resistors and with
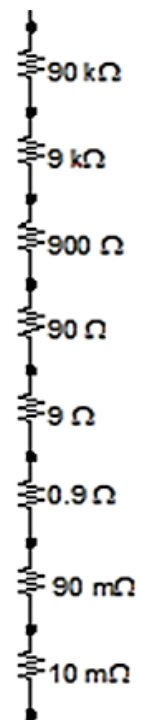

Figure 1. Scheme of the INRIM divider for generation of DC Voltage low values.

Figure 2. Photo of the INRIM divider in which the copper box housing the resistors can be seen.

incorrect operation. Solderings have been carried out with tin and specific flux for low emfs.

2.1 Building Details

In Figure 2, a photo of the developed prototype shows its characteristics. The resistors of the resistive divider, to avoid immunity to electrical and thermal noises, have been inserted in a milled copper box. The connections of the relays to the resistors are very short to reduce the parasitic resistance of the connections by bringing them as close as possible to the copper box to improve also the thermal anchoring.

The resistors are placed in the box cavities by means of a thin layer of thermally conductive paste.

It can be noted in Figure 3 a series of additional components wired on the welding side of the printed circuit have been added to allow a four-wire connection.

3. Calibration of the INRIM DC Voltage Divider

It is possible to calibrate the divider when necessary updating the values its division ratios. A quick calibration can be made with the setup of Figure 4. This setup involves the DMM [15], characterized in linearity on its 10 V range according to the suggestion of

satisfactory short-term stability. The bistable relays have a very low contact resistance (few mΩ) being equipped with silver/gold contacts with thermo-electromotive forces (emfs) on closing contacts extremely small $\cong 40$ nV.

To reduce noises due to self-heating or to electrical noise due to their excitation and maintenance voltages, taking advantage of their bistable feature, their activation is made by coupling their coils in alternating regime sending them only a single pulse. Therefore, no voltage is present during the measurement on the excitation coils of the relays.

The printed circuit, in vetronite, has been built with a micro milling technique for ultra-high insulation between the tracks. The coppered surfaces have double thickness than the standard. Resistors have been inserted in holes of a copper box to minimize electrical and thermal noises.

Relays connections have been made very short to minimize parasitic

resistances. The coils and the control circuits are supplied by a 5 V DC Voltage avoiding the noise due to mains. The condition of the relays, that currently is carried out with switches, is constantly monitored by switching on LED diodes, activated by auxiliary contacts, allowing to detect a possible
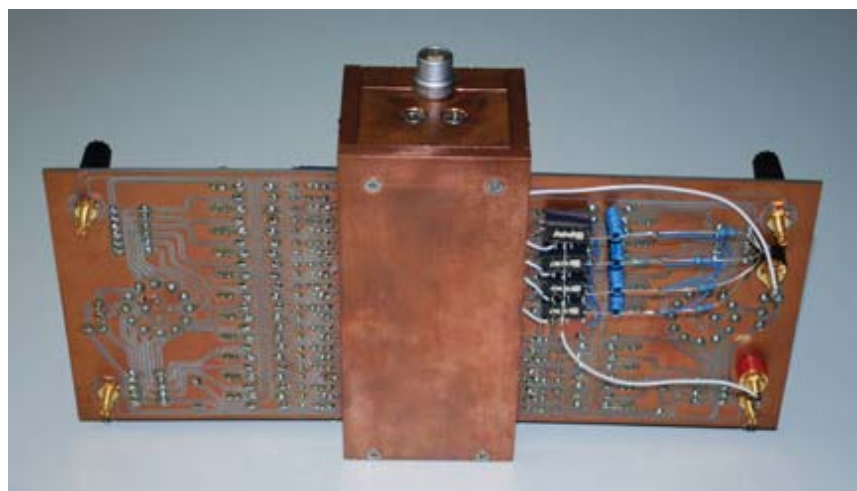

Figure 3. Photo of the connections of the resistors of the INRIM divider, allowing their connection in four terminal configuration.
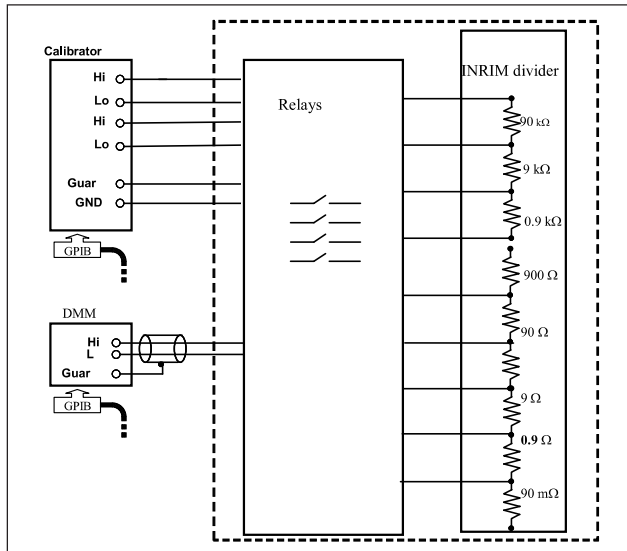
Figure 4. Measurement setup for the quick calibration of the divider.

[16, 17] and a J. Fluke 5700A calibrator [18] as high stability DC voltage generator. The measurement process, for each section, consists in measuring the input voltage to a section of the divider and alternatively the output voltage ten times lower. A DC Voltage from a high stability DC Voltage calibrator is applied to a resistor, while the DMM measures this voltage. Then, leaving unchanged the supplying point and the voltage value, the DMM measures the voltage on the one-decade lower value resistor without changing the DMM range. Applying in cascade this strategy from the first section to the last one, it is possible to obtain the ratios from 10:1 to 10^7:1.

For example, considering the section with an input resistance of 10 kΩ, the 10 V (LV_H) voltage is applied to it and measured with the DMM on the 10 V range. Then,

leaving unchanged the point of application of the 10 V, the DMM will read the output on 900 Ω corresponding (taking into account the resistors from 900 Ω to ground) at 1 kΩ (1 V always on the 10 V range). The ratio between the input on 10 kΩ and the output on 900 Ω will therefore be evaluable. The same procedure is repeated at −10 V to lower the emfs' effects. The procedure can include, before the polarities reversal and before each section change, the evaluation of the zeroes to be eventually added to the following DMM readings. The unknown ratio values are given, meaning the values at both polarities from the following relation:

$$R = \frac{L_H}{L_L} \qquad (1)$$

Where $R \cong 10$ is the ratio, L_H is the supplying voltage while L_L is the voltage on the lower decade resistor. Those voltages are the mean voltage readings by the DMM. In Table 1, the involved resistors, voltages and DMM readings for each divider section, are listed.

To take into account all the input quantities in the uncertainty budget for the quick calibration of the divider, Equation 1 can be re-written as:

$$R = \frac{L_H \varepsilon_{linH} \varepsilon_G \varepsilon_{emfH}}{L_L \varepsilon_{linL} \varepsilon_{emfL}} \cdot \varepsilon_{load} \qquad (2)$$

where

ε_{linH} and ε_{linL} are the DMM linearity specifications at 10 V and 1 V;

ε_{iemf0} and ε_{iemfL} are the emfs when the DMM measures 10 V and 1 V; and

ε_G ε_{Vc} and ε_{load} are respectively the corrections due to the stability of calibrator to the voltage coefficient of the divider and to the load effect at the DMM input.

Section	Cumulative ratio	Resistor to apply V_H	Resistor to measure L_L	L_H (V)	L_L (V)	DMM range
1	10:1	90 kΩ	10 kΩ	10	1	10
2	10^2:1	9 kΩ	1 kΩ	10	1	10
3	10^3:1	900 Ω	100 Ω	1	0.1	1
4	10^4:1	90 Ω	1 Ω	1	0.1	1
5	10^5:1	9 Ω	1 Ω	0.1	10^{-2}	0.1
6	10^6:1	9 Ω²	90 mΩ	10^{-2}	10^{-3}	0.1
7	10^7:1	9 Ω³	10 mΩ	10^{-3}	10^{-4}	0.1

Table 1. Resistors, voltages and DMM ranges for the quick calibration of the **INRIM** divider.

2   V_H is applied on the 9 Ω resistor to avoid excessive load on the calibrator, but L_H is measured on the 900 mΩ resistor.
3   V_H is applied on the 9 Ω resistor to avoid excessive load on the calibrator, but L_H is measured on the 90 mΩ resistor.

Uncertainty component	type	1 δ (×10⁻⁷)
DMM noise of L_H	A	0.3
DMM noise of L_L [4]	A	0.1
ε_{linH}	B	0.6
ε_{linL}	B	3.2
ε_G	B	2.3
ε_{eVc}	B	0.0
ε_{emfH}	B	0.1
ε_{emfL}	B	1.2
ε_{load}	B	5.8 [5]
RSS		7.1

Table 2. Standard uncertainties of the quick calibration of the first section of the INRIM divider.

3.1 Uncertainties of the Quick Calibration Method of the Divider

Let's consider the example of the previous paragraph where on an input resistance of 10 kΩ is supplied a DC Voltage of 10 V. Taking advantage of the DMM linearity specifications on the 10 V range, it is possible to consider these instead of the much larger accuracy specifications. In Table 2, the standard uncertainties budgets of the quick calibration of this section is given.

In these first characterization measurements, the divider has been used in a semi-automated way, selecting the desired ratios by means of a rotary selector. In Table 3, the standard uncertainties of each section and the standard uncertainty of the cumulative ratios are reported. These ratios are obtained multiplying the 10:1 ratios of each section.

The uncertainties of the cumulative ratios are evaluated taking into account a partial correlation ($r = 1.6 \times 10^{-4}$) in the evaluation of the cumulative ratios. r was evaluated according to [19].

4. Discussion

In addition to the described work, an analysis of the short-time stability of the divider ratios has been made. In fact, as it can be quickly calibrated when necessary and involved in traceability transfer, as its involvement in a

---

4   This measurement is also made on the 10 V range of the DMM.
5   This component has been evaluated considering the input impedance of a specific DMM. The input impedance of the available DMMs model [15] were accurately measured and the item with the higher impedance (about $8.6 \times 10^{11} \Omega$) was enrolled in the measurement setup.

Section	u (×10⁻⁶)	Cumulative Ratios	u (×10⁻⁶)	U (×10⁻⁶)
1	0.7	10:1	0.7	1.4
2	0.4	10^2:1	0.9	1.7
3	1.5	10^3:1	1.7	3.4
4	1.5	10^4:1	2.3	4.6
5	6.8	10^5:1	7.0	14
6	30.5	10^6:1	32.5	63
7	296	10^7:1	296	592

Table 3. Uncertainties of the quick calibration of each section and of the cumulative ratios of the INRIM divider.

measurement setup for calibration of nanovoltmeters. Table 4 shows the preliminary values of the short-time (a week) stability of the divider ratios.

These values have to be confirmed in successive evaluations of the divider and inserted in the divider use uncertainties [20], along with its calibration uncertainties. Use uncertainties are those with which it can be used when involved in calibration of other instrumentation though the divider is realized to be quickly calibrated when necessary to be used shortly after. In Figure 5 (on the following page), the mid-term stability of the 10:1 ratio is shown.

5. Conclusion

From the preliminary results, the performance of the divider is satisfactory. Next work will be the full automation of the divider and the further verification of its measurement noises in the calibration and of its short-term stabilities. Consequently, the calibration uncertainties could be re-evaluate as well as its short-time use uncertainties can be evaluated. Latest results will be soon published on a top metrology journal. These results include the divider automation, the evaluation of the divider: short and mid-term drift, temperature dependence, calibration and use uncertainties for all the ratios. In addition, the description of a solution to minimize the DMM load effect and the emf effect of the relays will be given.

Ratios	Stability (×10⁻⁶)
10:1	0.3
10^2:1	0.3
10^3:1	0.7
10^4:1	0.8
10^5:1	6.6
10^6:1	47.6
10^7:1	446

Table 4. Short-time stability of the divider ratios.
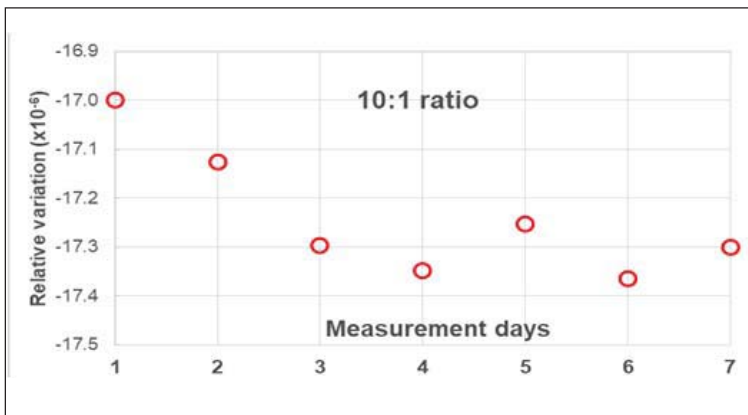
Figure 5. Mid-term stability of the 10:1 ratio.

References

[1] Fluke Corporation, *752A Reference Divider Instruction Manual*, Rev. 1, (1984).

[2] Fluke Corporation, *720A Kelvin Varley Voltage Divider Instruction Manual*, Rev. 2, (1974).

[3] F.Galliana, P.P. Capra R. Cerri, M. Lanzillotti, "Automated precision DC voltage fixed ratios divider," *Measurement* 122, pp. 291-296, (2018).

[4] Y. Sakamoto, T. Endo, H. Shao, S. Matsuzawa, "Automated DC voltage divider to calibrate voltages up to 1 kV," *Prec. El. Measur. Conf. (CPEM)*, pp. 363-364, (2000).

[5] Tadashi Endo, Y. Sakamoto, T. Sakuraba, Y. Murayama, H. Yoshida, A. Odawara, "Automated voltage divider to calibrate a 10-V output of Zener voltage standard," *IEEE Trans. Instrum. Meas.* Vol 40 (2), pp. 333- 336, (1991).

[6] H. Hirayama, M. Kobayashi, K. Murakami, T. Kato, "10-kV High-Accuracy DC Voltage Divider," *IEEE Trans. Instrum. Meas.* 23 (4) pp. 314- 317, (1974).

[7] Yi Li, Miyuru K. Ediriweera, F. S. Emms, A. Lohrasby, "Development of Precision DC High-Voltage Dividers," *IEEE Trans. Instrum. Meas.* 60 (7), pp. 2211-2216 (2011).

[8] R.B.D. Knight; P. Martin, "A high voltage divider having an uncertainty of 5 p.p.m. at 100 kV," *IEEE Trans. Instrum. Meas.* 42 (2), pp. 568-570, (1993).

[9] N. Dragounova, "Precision high-voltage dc dividers and their calibration," *IEEE Trans. Instrum. Meas.* 54 (5), pp. 1911-1915, (2005).

[10] A. Merev, Ö. Kalenderli, "The construction of a DC high voltage precision divider," *Journal of Electrostatics* 67 (5) pp. 741-745, (2009).

[11] D. Georgakopoulos, S. Grady, I. Budovsky, S. Grady, "Quantum Calibration System for Digital Voltmeters at Voltages from 10 nV to 1 kV," *IEEE Trans. Instrum. Meas.* 62 (6), pp. 1581-1586, (2013).

[12] H. E van den Brom, E.t Houtzager, G. Rietveld, R.van Bemmelen and O. Chevtchenko, "Voltage linearity measurements using a binary Josephson system," *Meas. Sci.Tech*, 18, (11), (2007).

[13] M. Early, L. Christian, T. B. Lawson, "Building-up the DC voltage scale for sources and meters," *29th Prec. El. Measur. Conf. (CPEM)*, pp. 138-139, (2014).

[14] D. Aviles, E. Navarrete, "DC Voltage Traceability to Microvolt and Nanovolt Levels," *31st Prec. El. Measur. Conf. (CPEM)*, pp. 138-139, DOI: 10.1109/CPEM.2018.8501104, (2018).

[15] Agilent Technologies, *3458A Multimeter - User's Guide*, rev. B.01, (2001).

[16] A. Sosso, R. Cerri, "Calibration of multimeters as voltage ratio standards," *Prec. El. Measur. Conf. (CPEM)*, pp. 375-376, (2000).

[17] J. I. Giem, "Sub-ppm linearity testing of a DMM using a Josephson Junction array," *IEEE Trans. Instrum. Meas.*, 40 (2), 329-332, (1991).

[18] Fluke Corporation, *5700A/5720A Series II Multi-function Calibrator Service Manual*, Rev. 1, 3/02, (June 1996).

[19] F.Galliana, M. Lanzillotti, "Accurate comparison between INRIM and a secondary calibration laboratory using a top-class multifunction electrical calibrator," *Measurement* 103, pp. 353-360, (2017).

[20] W. Bich, F. Pennecchi, "On the In-Use Uncertainty of an Instrument," *Advanced Mathematical & Computational Tools in Metrology*, World Scientific, pp. 159-169, (2004).

Flavio Galliana (f.galliana@inrim.it), Roberto Cerri, Davide Corona, National Institute of Metrological Research, Strada Cacce, 91, 10135 Turin, Italy.